

FORM PTO-1449

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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IMEC65.1PCP1APPLICATION NO.
09/273,089INFORMATION DISCLOSURE STATEMENT
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Schaumont et al.FILING DATE
March 19, 1999GROUP
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
RF	1	5,623,418	4/22/1997	Rosotker et al.	716	1	
RF	2	5,933,356	8/1999	Rostoker et al.	703	18	
RF	3	6,324,678	11/2001	Damgelo et al.	716	18	

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	4	0 445 942 A2	2/25/1991	EP	—	—		

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)		
RF	5	Altmeyer, et al. "Generating ECAD Framework Code from Abstract Models," Proc. of the 32nd ACM/IEEE Conf. on Design Automation, 1995, pp. 88-93.	
RF	6	Basu, et al. "DOORS: An Object-Oriented CAD System for High Level Synthesis," IEE Proceedings - Computers and Digital Techniques, 1997, Vol. 144, Issue 5, pp. 331-342.	
RF	7	Bill Lin. "System Design Tools for Broadband Telecom Network Applications" XP-002114729, 1996, pp. 23-26.	
RF	8	Bolsens, et al. "Hardware/Software Co-Design of Digital Telecommunications Systems," Proceedings of the IEEE, 1997, Vol. 85, Issue 3, pp. 391-418.	
RF	9	Bredenfeld et al. "Tool Integration and Construction using General Graph-based Design Representations," Proc. of the 32nd ACM/IEEE Conf. on Design Automation, 1995.	
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RF	11	Chao, et al. "Efficient Retiming and Unfolding," IEEE Inter. Conf. on Acoustics, Speech and Signal Processing, 1993, Vol. 1, pp. 421-424.	
RF	12	De Micheli, et al. "The Olympus Synthesis System," IEEE Design & Test of Computers, 1990, Vol. 7, Issue 5, pp. 37-53.	
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16 Moon, et al. "An Object-Oriented VHDL Design Environment," Proc. of the 27th ACM/IEEE Design Automation Conf., 1990, pp. 431-436.

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